Aligned with global growth

VAT benefits from exposure to the secular growth story in the semiconductor market. Shrinking node sizes also allow VAT to outgrow market trends.

Growth factor 3: Increasingly complex manufacturing

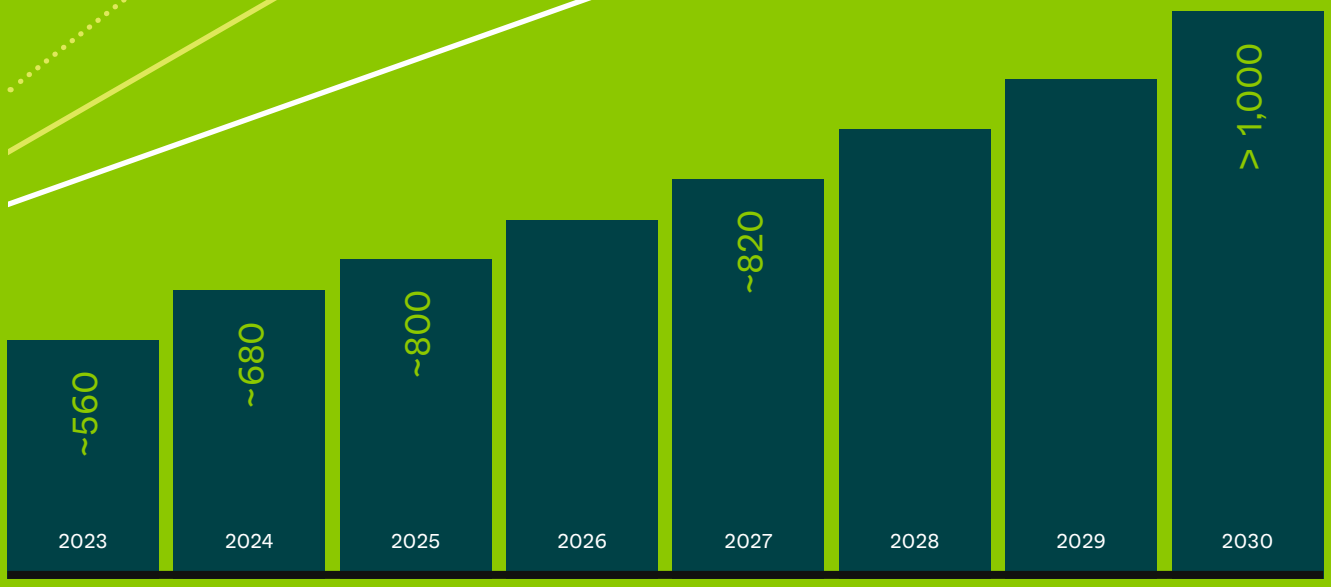
Growth factor 2: Evolving manufacturing technology

Growth factor 1: Data society

Semiconductor market to reach

> one trillion

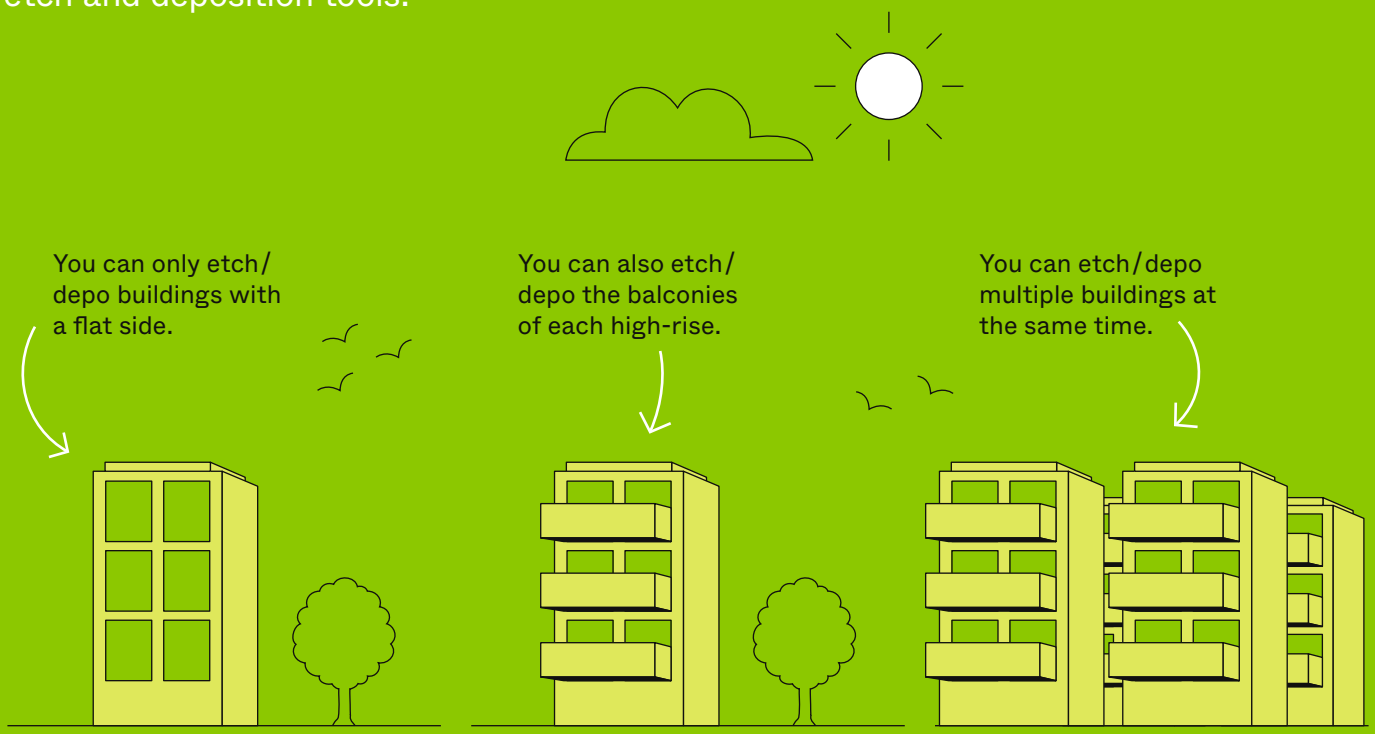
USD by 2030



Size of semiconductor market, 2023 to 2030 (USD billion)
Source: TechInsights (December 2024)

Imagine a semiconductor chip as a small city with high-rise buildings.

Shrinking node sizes and new architectures require more advanced etch and deposition tools.



Up to 2024

FinFET
(early 3D structure)

Current transistor structure has replaced CMOS structures thanks to to superior switching times and density.

2025 onwards

GAA
(1st fully 3D structure)

Gate all-around transistors are successors to FinFET. Here, logic gates surround the channel on all sides, allowing smaller node sizes and higher performance.

2032 onwards

CFET
(stacking structure)

Below 1nm, CFET will be the transistor structure that allows channel width to be maximized going forward.